

HIMA F8621A

F8621A: Coprocessor module
use in in PES H51q

The coprocessor module has its own microprocessor HD 64180 and operates with a clock frequency of 10 MHz. It contains mainly the following functions:

- 384 kbyte static memory, CMOS-RAM and EPROM on 2 ICs. Battery buffer of the RAMs on power supply monitoring module F 7131.
- 2 interfaces RS 485 (half-duplex) with galvanic isolation and own communication processor. Transmission rates (set by software): 300, 600, 1200, 2400, 4800, 9600, 19200, 38400, 57600bps or takeover of the values which are set on the CU via DIP switch.
- Dual Port RAM for fast memory access to the second central module.

Space requirement 4 TE

Operating data 5 V DC: 360 mA

Pin allocation of the interface channels RS 485

Pin, RS 485 ,Signal, Meaning

- 1 ,-, -, not used
- 2 ,-, RP, 5 V decoupled by diodes
- 3, A/A, RxD/TxD-A, Receive/Transmit-Data-A
- 4 ,-, CNTR-A, Control signal A
- 5, C/C ,DGND ,Data Ground
- 6 ,-, VP, 5 V positive pole of power supply
- 7 ,-, -, not used
- 8, B/B ,RxD/TxD-B ,Receive/Transmit-Data-B
- 9 ,-, CNTR-B ,Control signal B

- Microprocessor HD 64180 with a clock frequency of 10 MHz
- Operating system EPROM
- RAM for a PLC master project

Note:

The RAM for the master project is buffered via the batteries on the monitoring module.

- Two interfaces RS 485, via communication software building block

You can contact us through the following contact information

setting of the baud rate up to 57600 bps

- Dual port RAM (DPR) for the communication with the central module via Bus 1.



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